

Attorney Docket No.:SAM-0466 Application Serial No.: 10/645,018 Reply to Office Action of: December 1, 2004

TES PATENT AND TRADEMARK OFFICE

Applicant(s): Jae-Hyung Lee

Examiner: Wells, Kenneth B.

Serial No.:

10/645,018

Group Art Unit: 2816

Filing Date:

August 21, 2003

Title:

SEMICONDUCTOR MEMORY DEVICE HAVING PARTIALLY

CONTROLLED DELAY LOCKED LOOP

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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Sir:

The following is in response to the Office Action dated December 1, 2004, and is filed within the shortened statutory period of three months. Please amend the application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.

PTO/SB/06 (08-03)
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PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875										Application or Docket Number			
CLAIMS AS FILED - PART I (Column 1) (Column 2)								SMALL ENTITY		OR	OTHER THAN SMALL ENTITY		
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